

PTO/SB/08a/b (08-03)

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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete If Known	
Sheet	1	of	1	Application Number	09/192,205-Conf. #9482
				Filing Date	November 13, 1998
				First Named Inventor	Gail A. Alverson
				Art Unit	2127
				Examiner Name	K. Tang
				Attorney Docket Number	324758001US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MA		US-6,560,626-B1	05-06-2003	Hogle et al.	
MA		US-6,584,489-B1	06-24-2003	Jones et al.	

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Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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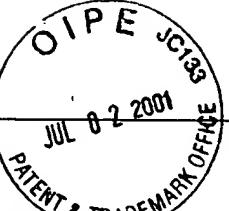
Examiner Signature	Majid Banoubali	Date Considered	9/14/04
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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COMPLETE IF KNOWN

Application Number	09/192,205
Confirmation Number	9482
Filing Date	November 13, 1998
First Named Inventor	Gail A. Alverson
Group Art Unit	2756
Examiner Name	

Sheet 1 of 4 Attorney Docket No. 324758001US

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*EXAMINER INITIALS	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		Office	Number				
MJ	AM	DE	DE19710252	Fujitsu Ltd. (English Abstract)	2/26/98		
	AN						

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

*EXAMINER INITIALS	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.	T
MJ	AO	Gail Alverson et al., "Tera Hardware-Software Cooperation," Proceedings of Supercomputing 1997, San Jose, California, November 1997.	
MJ	AP	Jack W. Davidson and David B. Whalley, "Reducing the Cost of Branches by Using Registers," Proceedings of the 17th Annual Symposium on Computer Architecture," Seattle, Washington, May 28-31, 1990.	
MJ	AQ	Jens Knoop et al., "The Power of Assignment Motion," ACM SIGPLAN '95 Conference on Programming Language Design and Implementation," La Jolla, California, June 18-21, 1995.	

EXAMINER

Mary B. Montebello

DATE CONSIDERED

9/14/04

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<p style="text-align: center;">O I P E JONES INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i> <small>Substitute for form 1449A/PTO</small> <small>JUL 02 2001</small> <small>PATENT & TRADEMARK OFFICE</small> </p>				COMPLETE IF KNOWN	
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MB	AM	Tomas Lang and Miquel Huguet, "Reduced Register Saving/Restoring in Single-Window Register Files," Computer Architecture News, Vol. 14, No. 3, June 1986.	
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MB	AP	Jenn-Yuan Tsai et al., "Performance Study of a Concurrent Multithreaded Processor," IEEE, 1998. pp. 24-35.	

EXAMINER*Majid Barankhali***DATE CONSIDERED***9/14/04*

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		NUMBER	Kind Code (if known)			
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MB	AL	"Method of Tracing Events in Multi-Threaded OS/2 Applications," IBM Tech. Disclosure Bulletin, September 1993, pp. 19-22.	
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MD	AP	Preston Briggs et al, "Coloring Heuristics for Register Allocation," Department of Computer Science, Rice University, Houston, Texas, June 1989.	

EXAMINER	<i>Maryl Boen</i>	DATE CONSIDERED
		9/14/04

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	AK							
	AL							
	AM							
	AN							
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AB	AP	SangMin Shim and Soo-Mook Moon, "Split-Path Enhanced Pipeline Scheduling for Loops with Control Flows, IEEE, December 2, 1998.	
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EXAMINER <i>Majid Banavharkar</i>	DATE CONSIDERED <i>9/14/04</i>
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